

EORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. 30585/9		SERIAL NO. 09/425,401	
<b>INFORMATION DISCLOSURE CITATION</b> (Use several sheets if necessary)					APPLICANT John S. Yates, Jr., et al.			
					FILING DATE October 22, 1999		GROUP ART UNIT 2751	
<b>U.S. PATENT DOCUMENTS</b>								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
		3,544,969	12/01/70	Rakoczi	340	172.5	Nov. 27, 1967	
		3,781,823	12/25/73	Senese	340	172.5	July 28, 1972	
		4,084,235	04/11/78	Hirtle	364	200	Apr. 14, 1975	
		4,455,602	06/19/84	Baxter	364	200	May 22, 1981	
		4,575,797	03/11/86	Gruner	364	200	May 22, 1981	
		5,638,525	06/10/97	Hammond	395	385	Feb. 10, 1995	
		5,796,939	8/18/98	Berc, et al.	395	184.01	Mar. 10, 1997	
		5,802,272	9/1/98	Sites, et al.	395	183.21	Dec. 19, 1994	
		5,822,578	10/13/98	Frank	395	591	June 5, 1995	
		5,832,205	11/03/98	Kelly	395	185.06	Aug. 20, 1996	
		5,930,509	07/27/99	Yates	395	707	Jan. 29, 1996	
		5,958,047	09/28/99	Panwar	712	237	Jun. 25, 1997	
<b>FOREIGN PATENT DOCUMENTS</b>								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
		WO 96/24895	08/15/96	PCT/US	G06F	9/22		
<b>OTHER DOCUMENTS</b> <i>(Including Author, Title, Date, Pertinent Papers, Etc.)</i>								
		Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)						
		Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)						
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		T.M. Conte, et al., "Accurate and practical profile-driven compilation using the profile buffer," Proceedings of the 29 <sup>th</sup> Annual International Symposium on Microarchitecture, Paris, France, pp. 36-45 (Dec. 1996)						
		T.M. Conte, et al., "Hardware based profiling: An effective technique for profile-driven optimization," International Journal of Parallel Programming, vol. 24, no. 2 (Feb. 1996)						
		T.M. Conte, et al., "Using branch handling hardware to support profile-driven optimization," Proceedings of the 27 <sup>th</sup> Annual International Symposium on Microarchitecture San Jose, CA (Dec. 1994)						
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<i>Chan, G.</i>					<i>4-15-04</i>			

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EORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 30585/9	SERIAL NO. 09/425,401
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<b>OTHER DOCUMENTS</b> (Including Author, Title, Date, Pertinent Papers, Etc.)					
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			K.N.P. Menezes, "Hardware-based profiling for program optimization," Ph.D. thesis, Department of Electrical and Computer Engineering, North Carolina State University, Raleigh, North Carolina (1997)		
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5231.9-4016SERIAL NO.  
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John S. Yates, Jr., et al.

FILING DATE

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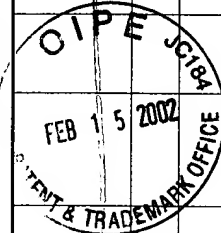
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Not Considered	Anton Chernoff, et al., FX!32, A Profile-Directed Binary Translator, IEEE Micro, Vol. 18 No. 2 pp. 56-64 (March/April, 1998)

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<b>INFORMATION DISCLOSURE CITATION</b> (Use several sheets if necessary)				APPLICANT John S. Yates, Jr., et al.	
				FILING DATE October 22, 1999	GROUP ART UNIT 2751 2124
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		T.M. Conte, et al., "Hardware based profiling: An effective technique for profile-driven optimization," International Journal of Parallel Programming, vol. 24, no. 2 (Feb. 1996)			
		T.M. Conte, et al., "Using branch handling hardware to support profile-driven optimization," Proceedings of the 27 <sup>th</sup> Annual International Symposium on Microarchitecture San Jose, CA (Dec. 1994)			
		J.S. Cox et al., "Commercializing profile-driven optimization." Proceedings of the 28 <sup>th</sup> Hawaii International Conference on System Sciences, vol. 1 Maui, HI, pp. 221-228 (Jan. 1995)			
		Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) <a href="http://www.digital.com/semiconductor/amt/fx32/fx-white.htm">http://www.digital.com/semiconductor/amt/fx32/fx-white.htm</a>			
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		R.E. Hank, et al., Proceeding of the 28 <sup>th</sup> Annual International Symposium on Microarchitecture, pp. 158-168 (Dec. 1995)			
		Raymond J. Hookway, et al., Combining Emulation and Binary Translation (August 28, 1997) <a href="http://www.digital.com/info/DTJP01HM.HTM">http://www.digital.com/info/DTJP01HM.HTM</a>			
		Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)			
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EXAMINER <i>Chao, J</i>		DATE CONSIDERED 4-14-04			

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<b>INFORMATION DISCLOSURE CITATION</b> (Use several sheets if necessary)				APPLICANT John S. Yates, Jr., et al.	
				FILING DATE October 22, 1999	GROUP ART UNIT <del>2751</del> 2124
<b>OTHER DOCUMENTS</b> (Including Author, Title, Date, Pertinent Papers, Etc.)					
Not Considered ↓ J I P E FEB 15 2002 TRADEMARK OFFICE ↓ Not Considered				Nanodata Corporation, QM-1 Hardware level User's Manual (1975)	
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EXAMINER Chris, G.			DATE CONSIDERED 4-14-04		

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John S. Yates, et al.

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245T 2/24

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						YES	NO

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